



Personal Data

Name, First Name Maletz, Thomas
Address Hauptstraße 101e
82008 Unterhaching
Germany
Telephone +49 (0) 15115678913
E-Mail thomas.maletz@maletz.de
Nationality German
Birthday 23. Dezember 1969
Place of birth Munich
Marital Status Married

Profession

Since 1.11.2019 **Apple**
RF Verification

After Apple acquired the Intel mobile communication continued the former tasks at Intel:

1.7.2005 – 31.10.2019 **Intel**
RF Verification and System Engineering
Member of Technical Staff

- TX and PLL Verification of mobile communication transceiver-ICs (GSM, UMTS, LTE, NR)
- End-to-end System Debugging incl. FW, Digital, Generation of signals
- Automation of measurements
- Feedback of verification results to Design
- DSP for mobile communication transmitter (filters, SystemC, MATLAB)
- Training of other teams
- R&D of test-chips for future generations of the most advanced transmitters in the world (e.g. modulated & digital PLL, C-DAC etc.)
- Winner of many awards

Infineon Technologies AG, Senior Engineer RF Verification and Module Design

- Verification of transceiver-ICs for mobile communication (receiver, transmitter, synthesizer, ...); GSM, EDGE, UMTS, WiMAX
- Customer support
- Automation of measurements
- Feedback of verification results to Design
- Design of a module:
Ceramic substrate with transceiver and integrated frontend (RX SAW Filter, TX LP Filters, Switch)

15.9.2004 – 30. Juni 2005

Senior Consultant with Ogado

Projects at Infineon

- Verification of transceivers for GSM mobile phones with focus on transmitter and synthesizer
- Output power
- Modulation performance and spectrum
- Phase noise, pushing
- Building of LabView-Programmes for automated measurements
- Feedback of verification results to Design

1.9.1999 – 31.12.2003

Radio frequency Engineer with Sony(Ericsson) Design and development of GSM mobile phones

- Working on various projects and mobile phones
- LNA of receiver: transistors with matching
- Synthesizer (IC): performance checks, above all settling time, phase noise
- Development of various RF circuits like filters, matching networks etc...
- Simulations with ADS and PSpice
- Temperature behaviour of power amplifiers (measurements and simulations)
- Timing of the RF part and its programming (above all transceiver and synthesizer)
- Verification of the RF performance of the developed mobile phones, manually and automated
- On the spot support of production (engineers) in Japan and France
- Troubleshooting for Hard-/Software during production
- Introducing hardware and software for mass production in development (calibration-/test-station, SW in C++)
- Interface between production and development
- Company language at Sony(Ericsson) was English

Awards at Sony(Ericsson)

- Proposal of cooperation between SONY, the „Deutsches Zentrum für Luft- und Raumfahrt“ (DLR; engl.: German Aerospace Center) and further companies like SUN Microsystems for the „Heywow“ - Project, that was developed by the DLR. Heywow was also supported by the State of Bavaria. Project goals were new applications of mobile phones. Proposal of various ideas for new applications and establishing the contact to the DLR.
- Winner of a prize for ideas for developing the contact between SONY, universities and students.
- Prize for patent ideas.

Resume

Thomas Maletz

Second Studies

SS2012 **Studies of Physics at the Ludwig Maximilian University of Munich**

Thesis: A Small Radio-Telescope

WS2014/15 **B.Sc. Physics**

WS2001/02 **Studies of Mathematics at the Open Univ. Hagen**

Minor Electrical Engineering

Fall 2003 „Vordiplom“ finished

Studies

WS1990/91 – SS1992 **Studies of Physics at the Technical Univ. Munich**

WS1992/93 – SS1995 **Studies of Electrical Engineering at the TU Munich**

In parallel computing science

Basics /Introductions (4 terms) and
Operating systems (1 term)

SS1996 – SS1999 **Studies of Electrical Engineering at the FH Munich
(University of applied science)**

Specialized on communication technologies

Thesis at Rohde & Schwarz

"Development of a broadband directional coupler"

Usage of the directional coupler in radios for
Air Traffic Control

Title (German degree in electrical and electronic engineering):
Diplom-Ingenieur (FH) Elektrotechnik

Practical trainings during the studies

5 months **DASA**
Programming of a digital circuit for controlling
a CCD (Cameras for Aerospace) in VHDL.

5 months **Rohde & Schwarz**
Building and verification of PIN-diode-switches and
drivers, IQ-modulator, directional coupler, filters.

FH Munich

Building of a Spread Spectrum (Direct Sequence)
receiver with MatLab.

Resume

Thomas Maletz

National Service

5.06.1989 – 31.08.1990 Airforce (Erding near Munich)

School

1976 – 1978 Grundschule Canisius
1978 – 1980 Grundschule am Hedernfeld
1980 – 1989 Erasmus-Grasser-Gymnasium
Graduated with „**Abitur**“

Additional Qualifications

UNIX, Windows, DOS, Mac
Office
Python, C(++), Pascal, HTML
VHDL
LabView, MatLab, Mathematica
ADS, SPICE, Serenade
Cocentric System Studio, SystemC
LATEX
Englisch
Chinese (Basics, incl. reading & writing)

References

Further information and all references can be found at
<http://www.maletz.de/references.html>

Munich,
12. August 2022